

Re: Transmission Gate question

Source: <http://sci.tech-archive.net/Archive/sci.electronics.basics/2006-01/msg01477.html>

- *From:* Pooh Bear <rabbitsfriendsandrelations@xxxxxxxxxxxxx>
 - *Date:* Tue, 31 Jan 2006 12:10:02 +0000
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Angmor wrote:

- > Something I didn't make entirely clear.... I don't understand how they
- > can just state that the output voltage follows the input voltage
- > closely. Where is the justification? This is the real problem I'm
- > having.

When the fets are on clearly this must be the case (assuming the load is much greater than the fets' on resistance of course – which is how you use these things).

Graham

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- *Follow-Ups:*
 - ◆ [**Re: Transmission Gate question**](#)
 - ◇ *From:* Noway2
- *References:*
 - ◆ [**Transmission Gate question**](#)
 - ◇ *From:* Angmor
 - ◆ [**Re: Transmission Gate question**](#)
 - ◇ *From:* Angmor
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