

PIC processor interrupt for delayed output

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What is the best approach to generate a delayed output from a trigger input to a PIC microcontroller? The idea is to have a constant known time from trigger input to output, so I don't want to poll the logic level on some pin due to the jitter that would occur. Thought about using the comparator module to generate an interrupt, but it seems complicated setting and clearing all the various bits and reference levels.

Is there an easier way?

-Bill

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