

# Re: PIC processor interrupt for delayed output

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  - *Date:* Tue, 22 Apr 2008 14:21:57 +0200
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What is the best approach to generate a delayed output from a trigger input to a PIC microcontroller? The idea is to have a constant known time from trigger input to output, so I don't want to poll the logic level on some pin due to the jitter that would occur. Thought about using the comparator module to generate an interrupt, but it seems complicated setting and clearing all the various bits and reference levels.

Is there an easier way?

-Bill

Assuming the trigger not to be synchronous with the PICs clock, you will always have some jitter. That will be always the case when using asynchronous systems.

As for the PIC, you did not mention what PIC you have in mind. The usual way is using the trigger signal to start an interrupt routine. This routine in turn starts a timer/counter that interrupts when delaytime is over. I see no use for comparators in this scheme.

petrus bitbyter