

Re: Controlled impedance (stripline etc.) boards

Source: <http://sci.tech-archive.net/Archive/sci.electronics.cad/2005-01/0087.html>

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Date: 01/06/05

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On 6 Jan 2005 07:24:31 -0800, the renowned optoeng@pioneernet.net wrote:

>Look for an article by Dr. Eric Bogatin in latest Printed Circuit
>Design. Don't know if it's on the web yet or not.
>Paul Mathews

If it's the January issue, it's here (3.9M zip file):

http://www.pcdandm.com/pcdmag/mag/0412/0412pcdm_digital.zip

But he doesn't seem to address the issue of guard traces in that article ("Quieting Down a Noisy Problem").

Best regards,
Spehro Pefhany

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"it's the network..."

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Embedded software/hardware/analog

"The Journey is the reward"

Info for manufacturers: <http://www.trexon.com>

Info for designers: <http://www.speff.com>