

Re: Wanted: LM-709 (Spice model) National Op-Amp

Source: <http://sci.tech-archive.net/Archive/sci.electronics.cad/2005-09/msg00148.html>

- *From:* Jim Thompson <thegreatone@xxxxxxxxxxx>
 - *Date:* Sun, 25 Sep 2005 09:20:09 -0700
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On Sun, 25 Sep 2005 10:17:33 GMT, Paul Rako <sp_a_mpa_u_1@xxxxxxxxxxx> wrote:

[snip]

>

>Mr. (Dr./Prof?) Thompson:

"Mr.", I only have a MSEE.

>What do you use? I hope it is not some proprietary thing
>like Analog Devices' internal SPICE.

I use PSpice A/D v10.5, although I'm trying to make time to evaluate a newcomer, TTSpiceWorks <<http://www.trabucotechnologies.com/>>. But it seems, the older I get the busier I get ;-)

>I hope you will serve as
>judge jury and executioner in our little test.

What "little test" is it? I haven't been following this thread closely.

>And remember
>everyone— in my opinion we are mis-applying SPICE. The
>acronym stands for "Simulation Program with Integrated
>Circuit Emphasis" It is not SPBLE. Spible would be
>"Simulation Program with Board-Level Emphasis".

Indeed.

>This is
>why there seems to be such passions aroused when us board
>guys say we don't worship SPICE the way IC designers do.

In the IC world we "back-annotate" to add the strays that result from layout. Do "board guys" have such a tool?

>BTW, Dave Tamura in the CAD department at National rolled his

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>eyes when I told him I said Process and Modeling departments
>cost 5 or 10 million dollars a year for a big semiconductor
>company. He hinted that tens times those numbers is not
>unheard of. He also said that his CAD department is also
>involved in making sure the models conform to reality.
>

[snip]

And Win wonders why discrete MOSFET models are inadequate ;-)

...Jim Thompson

—
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I love to cook with wine. Sometimes I even put it in the food.

• ***Follow-Ups:***

- ◆ ***Re: Wanted: LM-709 (Spice model) National Op-Amp***
 ◇ From: Paul Rako

• ***References:***

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 ◇ From: Paul Rako
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