

## Re: 74LS90, 74LS92 and other TTL are OBSOLETE???

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- *From:* "pdmtr" <pdmtr@xxxxxxxxx>
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Well, the truth is that there is a need for ONLY 2 TTLs in the design!!! Those are just used to give 6 different subclocks from a TCXO source. The design has also uCU, RF ASICs, and lot of other stuff like LCD, Keyboard, etc... It's sure not a "one chip" show :))

As I see it got to use a CPLD for replacement of the 2 TTL... the cheaper I have found until now is ~1.70\$ so that's far more than ~20cents!!! I'm sure not only mine but nobodies "boss" wouldn't like to spend 150K more than it could :)

Of course a 2U 19" rack mounted isn't so portable as a cell phone, so why we have to shrink everything we are doing in life even if there is plenty of room to use?

It's not look weird for you to open a 29" TV set and find out that all it is behind that large screen is a chip with say 3 drivers a HT T/R all in a board that could fit easy at 4.5" monitor? Yes, I know that's the way it goes... And it's sure more economical for a production line of >1M units / year to build your "all in one" ASIC! But at least we must admitte that is a bit weird :)) to shrink everything, even when there isn't a real need to do so!!!

I have done designs with FPGAs but there always a need to do it with. Either large complex circuitry, or even dynamic reconfiguration of the circuit needed and either space limits and total cost, the most cases was for portable devices.

But this is not always the case...

Nice to hear your opinions on this subject..

PS Thanks for Jameco... .