

Re: anyone familiar with this topology?

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Kevin Aylward wrote...

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> *The basic issue is that an op-amp has *lots* of poles, as it has many
> stages. Minimising the number of stages is usually the best thing to do.*

An over-conservative generalization. Lots of poles are not an issue if their frequency is 10 to 100x higher than the open loop bandwidth. The point of much opamp IC design is to smash everything into one or two dominate poles, and push the rest out to high frequencies.

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Thanks,

- Win

(email: use hill_at_rowland-dotties-org for now)