

sci.electronics.design: Re: Engineering Environment

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I read in sci.electronics.design that Kevin Aylward
<salesEXTRACT@anasoft.co.uk> wrote (in <O522d.4230\$U04.2409@fe1.news.blu
eyonder.co.uk>) about 'Engineering Environment', on Wed, 15 Sep 2004:

>*But you really need to understand the structure of the theory, to see
>just why it is a good theory.*

It's a real theory? Then what are its testable (preferably tested)
predictions?

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Regards, John Woodgate, OOO - Own Opinions Only.

The good news is that nothing is compulsory.

The bad news is that everything is prohibited.

<http://www.jmwa.demon.co.uk> Also see <http://www.isce.org.uk>