

Re: 4–Bit Divisor Implementation Using Logic Gates

Source: <http://sci.tech–archive.net/Archive/sci.electronics.design/2004–10/7150.html>

From: Andrew Holme (andrew_at_nospam.com)

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Junaid Uppal wrote:

> *Hello Everyone,*

>

> *I am currently working on a project to implement 4–bit division using*

> *logic gates / registers etc (Basic Level DLD Stuff). I've been*

> *searching online / offline for a lot of time now but haven't found*

> *anything that can help me implement my circuit. Can anyone help me out*

> *in making this circuit please? some pointers / logic diagrams or*

> *anything.*

>

> *Thanks & Regards*

>

> *Junaid Uppal*

Try something like

<http://www.google.com/search?q=division+restoring+quotient+remainder>

This page has some really neat Java applet demos

<http://tima–cmp.imag.fr/~guyot/Cours/Oparithm/english/Divise.htm>