Re: Common emitter amplifier question (rc || rl)

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From: lemonjuice (*exskimos_at_anonymous.to*)
Date: 02/01/05

Date: 1 Feb 2005 12:48:52 −0800

On Sun, 30 Jan 2005 09:06:35 +0000, John Woodgate
<jmw@jmwa.demon.contraspam.yuk> attempted to answer

>I read in sci.electronics.design that cheese9988
><cheese9988@hotmail.com> wrote (in
<1107034758.554400.279030@c13g2000cwb
>.googlegroups.com>) about 'Common emitter amplifier question (rc ||
>rl)', on Sat, 29 Jan 2005:
>>Hi, I have been trying to figure this out and its bugging me. On a
>>common emmitter amplifier, you have rl (load resistance) and rc
>>(collector resistor). How are these two in parallel with an ac
signal?
>>It looks more to me like rl is in parellel with the transistor and
both
>>being in series with rc. Can anyone explain this?
>>

>As far as the signal is concerned, the collector DC supply is at
ground
>potential (or should be).
>
stop wasting bandwidth by repeating his question. The question is
why is it at Ground. Either give an answer or go and mow your lawn.
Maybe you're Kevin Alyward under a different name?