

Re: Proposed Assembler Commands

Source: <http://sci.tech--archive.net/Archive/sci.electronics.design/2005-05/msg00106.html>

- *From:* kensmith@xxxxxxxxxxxxxxxx (Ken Smith)
 - *Date:* Sun, 1 May 2005 19:44:41 +0000 (UTC)
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In article <6sh871h65ksdq0trjpb19gurt7n0jmgdm8@xxxxxxx>, John Larkin <jjlarkin@xx> wrote: [...]
>I always liked the Moto instruction sets. The Intel stuff has always
>seemed klugey and disorganized to me. So, we all wound up with the
>worst instruction set and the worst OS the industry could invent.

You've obviously never seen the RCA CDP1802's instruction set.

Imagine you have 3, 1 byte variable in memory and wish to do $C = A + B$ here's the code for it (If my memory is working today):

```
LDI low(A) .. Load the low part of an address
PLO R2 .. Put into the low part of the reg
LDI high(A) .. Same for the high byte
PHI R2
```

```
LDI low(B) .. Load the low part of an address
PLO R3 .. Put into the low part of the reg
LDI high(B) .. Same for the high byte
PHI R3
```

```
LDI low(C) .. Load the low part of an address
PLO R4 .. Put into the low part of the reg
LDI high(C) .. Same for the high byte
PHI R4
```

```
LDN R2
SEX R3
ADD
STD R4
```

That right folks only 16 instructions needed for that.

—
kensmith@xxxxxxxxx forging knowledge

- ***Follow-Ups:***

- ◆ ***Re: Proposed Assembler Commands***
 ◇ *From:* keith
- ◆ ***Re: Proposed Assembler Commands***
 ◇ *From:* John Larkin
- ◆ ***Re: Proposed Assembler Commands***
 ◇ *From:* Larry Brasfield

- ***References:***

- ◆ ***Re: Proposed Assembler Commands***
 ◇ *From:* John Larkin
- ◆ ***Re: Proposed Assembler Commands***
 ◇ *From:* keith
- ◆ ***Re: Proposed Assembler Commands***
 ◇ *From:* John Larkin

- Prev by Date: ***Re: Help! uWave xistor bias oscillates!***
- Next by Date: ***Re: Industrial Metal Detection Ideas?***
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