

Re: Instruction And Data memory

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- *From:* anno4000@xxxxxxxxxxxxxxxxxxxxxxxxxxxx (Anno Siegel)
 - *Date:* 20 May 2005 11:02:15 GMT
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PeteS <ps@xxxxxxxxxxxxxxxxxxxx> wrote in sci.electronics.design:

- > I assume you are looking at a processor that has support for such
- > things (or a system that has such a processor or MMU).
- >
- > The difference is that instruction memory is exactly that: memory
- > referenced during an instruction fetch (the newer processors
- > discriminate between the various bus cycle types) and data memory is
- > for just that: data.

[snip explanation]

I'd make explicit that there is no technical difference between instruction memory and data memory, in case that was what the intent of the OP's question. They are the same kind of RAM since drum storage was scrapped. Any differences in behavior are by design, not by the nature of one or the other.

Instruction memory runs with von Neumann's blessings, data memory doesn't.

Anno

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- *Follow-Ups:*
 - ◆ ***Re: Instruction And Data memory***
 - ◇ *From:* Keith Williams

- *References:*
 - ◆ ***Instruction And Data memory***
 - ◇ *From:* thejim
 - ◆ ***Re: Instruction And Data memory***
 - ◇ *From:* PeteS

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