

Re: Instruction And Data memory

Source: <http://sci.tech-archive.net/Archive/sci.electronics.design/2005-05/msg03416.html>

- *From:* Keith Williams <krw@xxxxxxxxxx>
 - *Date:* Tue, 24 May 2005 09:14:44 -0400
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In article <111491pei07ncfqlfj2us29hp1fnq2k39k@xxxxxxx>, speffSNIP@xxxxxxxxxxxxxxxxxxxxxxxxxxxx says...

> On Mon, 23 May 2005 21:20:46 GMT, the renowned Rich Grise
> <richgrise@xxxxxxxxxx> wrote:
>
>> On Mon, 23 May 2005 13:56:58 +0000, Ken Smith wrote:
>>
>>> In article <pan.2005.05.22.18.27.37.278277@xxxxxxxxxx>,
>>> keith <krw@xxxxxxxxxx> wrote:
>>> [...]
>>>> Not at all. There is a rather big difference between the two. A modern
>>>> x86 doesn't do well with self-modifying code, but it can. An 8051 OTOH.
>>>> The first is clearly Princeton and the latter clearly Harvard.
>>>
>>> The PIC is a better example for Harvard. The 8051 uses the same bus but a
>>> different command signal. The PIC has a bus just for instructions.
>>>
>>> IIRC, I once scoured the PIC datasheet - A PIC datasheet - to see how
>>> hard a lookup table would be. Turns out it's a SOB.
>>>
>>> Thanks,
>>> Rich
>>>
>>> Got to be the only micro architecture that requires an application
>>> note to describe how to implement a LUT.
>>>
>>> Newer models are better, BTW, but you still have to be a bit careful
>>> compared to Von Neuman parts like the HC05/08.

Yeah, the '51 bends the Harvard rules by having a MOV_C to do LUTs.

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Keith

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- *References:*
 - ◆ *Instruction And Data memory*

Re: Instruction And Data memory

◇ *From:* thejim

◆ ***Re: Instruction And Data memory***

◇ *From:* keith

◆ ***Re: Instruction And Data memory***

◇ *From:* Fred Bloggs

◆ ***Re: Instruction And Data memory***

◇ *From:* keith

◆ ***Re: Instruction And Data memory***

◇ *From:* Ken Smith

◆ ***Re: Instruction And Data memory***

◇ *From:* Rich Grise

◆ ***Re: Instruction And Data memory***

◇ *From:* Spehro Pefhany

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