

# Better op-amp buffer configuration?

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Hi All,

I realize that using a single-supply op-amp as a buffer may be one of the easiest circuit configurations in the world; just tie the chip's output back to the inverting input, and insert the signal into its non-inverting input. That's how the books show it anyway, but are there any real-life op-amp issues that I have to look out for with this basic circuit? Anything I can do to make this circuit operate better under temperature, along with the unavoidable op-amp variations? Perhaps to help with any stability or voltage offset issues? Or is this circuit simply the way op-amp buffers are done, even in real life?

Thank You,

Bill

Cross posted to sci.electronics.design and sci.electronics.basics

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