

Re: need small PAL with SR latch

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- *From:* Joerg <notthisjoergsch@xxxxxxxxxxxxxxxxxxxxxxxx>
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Hello Colin,

all i need is 1 nor, 1 nand, 1 sr latch, 2 xor, and a 1 bit tri state buffer, I could probably do it with a very fast LUT too but im not sure if u can feedback data lines to address lines reliably unless its clocked, otherwise thats a few diferent packages even if i try and convert some of the gates etc.

A gall16v8z doesnt seem to consume much power unless they are hiding it well in the data sheet, oh wait thats in standby, at 10mhz its 55ma but im not pumping the clock line so maybe itl be a lot lower.

Look at "Normalized Icc versus Freq" in the lattice data sheet on page 19. It doesn't exactly appear to be a static CMOS behavior, there is a pretty stiff pedestal at 0MHz.

What really irks me and is acutally often the main reason why I do not use GALs in my designs is the cost. Your 16V8Z, for example, gobbles a whopping three to four (!) Dollars per chip. No thanks. I can do that with logic for 50 Cents or so and chances are pretty good that my clients can buy exactly those same logic chips 15 years from now without any problems. From half a dozen sources.

Regards, Joerg

<http://www.analogconsultants.com>

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