

Re: LVDS driver in SERDES

Source: <http://sci.tech--archive.net/Archive/sci.electronics.design/2005-11/msg00132.html>

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 - *Date:* 1 Nov 2005 19:20:27 -0800
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TI and national uses start bit and stop bit.

If the data runs continuously, how can the clock be embedded in them.

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- *Follow-Ups:*
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 - ◇ *From:* Fred Bloggs
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