

Re: LVDS driver in SERDES

Source: <http://sci.tech-archive.net/Archive/sci.electronics.design/2005-11/msg00143.html>

- *From:* Fred Bloggs <nospam@xxxxxxxxxxx>
 - *Date:* Wed, 02 Nov 2005 09:42:37 GMT
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Iterativeend wrote:

TI and national uses start bit and stop bit.
If the data runs continuously, how can the clock be embedded in them.

That clock is supposed to be CW, the data is parallel in, what do you think you're going to do- drill a hole in the package to access an internal node? The datasheet information is self-evident, how is it possible for anyone to become confused.