

# Re: HCT4051 leakage

---

*Source:* <http://sci.tech-archive.net/Archive/sci.electronics.design/2005-12/msg00120.html>

---

- *From:* "Mark" <[makolber@xxxxxxxxx](mailto:makolber@xxxxxxxxx)>
  - *Date:* 30 Nov 2005 17:16:24 -0800
- 

consider the capacitance at the input of the A/D.

when the mux changes channels, it has to charge/discharge to the new value through the series resistance

it might take a long time for the cap to reach final value accurate to 24 bits.

this leads to what appears to be crosstalk between the channels

you can improve the situation by setting the mux to channel with the fixed ref voltage in between reading active channels

it seems hard to believe a small c could do this but if you are going for 24 bits, it may be part of the problem

Mark

.

---

- *Follow-Ups:*

- ◆ [\*\*Re: HCT4051 leakage\*\*](#)  
◇ *From:* John Larkin

- *References:*

- ◆ [\*\*HCT4051 leakage\*\*](#)  
◇ *From:* John Larkin
- ◆ [\*\*Re: HCT4051 leakage\*\*](#)  
◇ *From:* Joerg
- ◆ [\*\*Re: HCT4051 leakage\*\*](#)  
◇ *From:* John Larkin

- Prev by Date: [\*\*BS2 microcontroller startup time\*\*](#)
- Next by Date: [\*\*Re: FM IF limiter\*\*](#)
- Previous by thread: [\*\*Re: HCT4051 leakage\*\*](#)
- Next by thread: [\*\*Re: HCT4051 leakage\*\*](#)
- Index(es):

Re: HCT4051 leakage

- ◆ Date
- ◆ Thread