

# warning in synplify

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*Source:* <http://sci.tech-archive.net/Archive/sci.electronics.design/2005-12/msg03938.html>

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  - *Date:* Thu, 22 Dec 2005 03:32:34 -0500
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Hi everybody,

I like know, what means three warning when I synthetize. If you can help me, you can send me a e-mail. thank you everybody.

42:49:42:49|Pruning Register i\_enc\_high(8 downto 0)

53:4:53:5All reachable assignments to i\_bitlen(0) assign '0', register removed by optimization

42:49:42:49|Optimizing register bit pixel\_cmpr(0) to a constant 0

28:9:28:16|Input low\_part is unused

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- *Follow-Ups:*

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  - ◇ *From:* Paul Burke

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