

# Re: Hans Camenzind's Book, Designing Analog Chips

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- *From:* Jim Thompson <[To-Email-Use-The-Envelope-Icon@xxxxxxxxxxxxxxxxxxx](mailto:To-Email-Use-The-Envelope-Icon@xxxxxxxxxxxxxxxxxxx)>
  - *Date:* Thu, 16 Feb 2006 07:54:04 -0700
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On Thu, 16 Feb 2006 02:51:44 GMT, Joerg  
<[notthisjoergsch@xxxxxxxxxxxxxxxxxxxxxxxxxxx](mailto:notthisjoergsch@xxxxxxxxxxxxxxxxxxxxxxxxxxx)> wrote:

Hello Jim,

I fitted a couple of designs onto the analog arrays... at customer insistence, then gave it up. MOSIS pretty much eliminates the need.

Yes, but that requires a full blown (and expensive) IC design. For really simple stuff it would be nice to live with what's provided and do the metal layout. Just like with gate arrays in the digital world. If they just hadn't limited it to 750ohm resistors.

Regards, Joerg

<http://www.analogconsultants.com>

Naaah. A MOSIS run is quite cheap. Each process has a "shuttle" run... multiple chip designs on a single wafer. Good cheap way for "proof of pudding".

As for arrays, I designed one for GenRad internal use that had more values, but it didn't see much use either.

And I might add... he who complains about resistor values shouldn't be trying to design an I/C ;-)

...Jim Thompson

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Anyone can be rude, but it takes a Democrat to be a real dirtbag.

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