

## Re: Initializing a Flip-Flop on Power-up

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- *From:* Jim Thompson <[To-Email-Use-The-Envelope-Icon@xxxxxxxxxxxxxxxxx](mailto:To-Email-Use-The-Envelope-Icon@xxxxxxxxxxxxxxxxx)>
  - *Date:* Fri, 17 Feb 2006 14:40:53 -0700
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On Fri, 17 Feb 2006 21:26:39 GMT, Joerg  
<[notthisjoergsch@xxxxxxxxxxxxxxxxxxxxxxxxx](mailto:notthisjoergsch@xxxxxxxxxxxxxxxxxxxxxxxxx)> wrote:

[snip]

Spending an extra 4-5 cents doesn't sound like you, what have you been drinking this afternoon? ;)

Oh, just coffee. But not the Douwe Egberts brand :-(

[snip]

Douwe Egbert makes coffee too? I used to smoke their Cavendish until the management decreed no smoking in the new house ;-(

Personal opinion: Anyone who relies on an edge detector for POR is a bit of a fool. In recent months it's been an almost weekly event to patch some chip that won't POR properly.

I prefer a level detect with hysteresis AND delay... easy to do if the chip design includes my bandgap reference ;-(

...Jim Thompson

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James E.Thompson, P.E.	mens
Analog Innovations, Inc.	et
Analog/Mixed-Signal ASIC's and Discrete Systems	manus
Phoenix, Arizona Voice:(480)460-2350	
E-mail Address at Website Fax:(480)460-2142	Brass Rat
<http://www.analog-innovations.com>	1962

It's what you learn, after you know it all, that counts.

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