

Re: Synchronous rectifier

Source: <http://sci.tech-archive.net/Archive/sci.electronics.design/2006-02/msg03712.html>

- *From:* "kell" <kellrobinson@xxxxxxxxxxxx>
 - *Date:* 21 Feb 2006 13:32:43 -0800
-

supermos@xxxxxxxx wrote:

Thank you for your reply!

I have said that the comparator is a zero-crossing detector. So it is used to compare V_{ds} with zero, but in fact there are some offset voltage in the input of the comparator. Thus the comparator point is actually \pm several mili volts. If the comparator point is positive several milivolts, there will be a large delay when turning off the MOSFET; a negative several milivolts will cause oscillation under light/zero load. Now you may understand it is very hard to have a hysteresis for such a comparator and in fact a hysteresis here is of no use.

This is for a VRM.

this article has some ideas

<http://www.edn.com/article/CA216166.html>

.