



Re: kablooey

We got one back yesterday. Two chips are running hot, the MC68332 CPU (directly on +5) and a big Xilinx SpartanXL FPGA. The FPGA is powered from +3.3, linear-regulated from the +5 supply, but it connects to the CPU bus, and it's supposed to be 5-volt tolerant on its I/Os. So it looks like the +5 blew the CPU and it, in turn, pulled up a bunch of the FPGAs i/o pins and fried it too.

What's cool is that the CPU is running very hot but is still executing the firmware! And the FPGA is hot and really dead.

We're going to replace both chips (418 pins total!) and see how things look. There are 5 more FPGAs on the board, but we're optimistic they're OK.

Here's a pic of the board.

<http://www.highlandtechnology.com/DSS/V375DS.html>

John