

Re: kablooey

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- *From:* ehsjr <ehsjr@xxxxxxxxxxxxxxxxxxxx>
 - *Date:* Fri, 10 Mar 2006 05:48:08 GMT
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John Larkin wrote:

On 9 Mar 2006 07:49:47 -0800, "Tim Shoppa" <shoppa@xxxxxxxxxxxxxxxxxxxx> wrote:

John Larkin wrote:

One of my better customers just called. Seems six of my VME arbitrary waveform generators failed, in the same VME crate, simultaneously. We're talking over \$30K of damage here.

<snip>

We got one back yesterday. Two chips are running hot, the MC68332 CPU (directly on +5) and a big Xilinx SpartanXL FPGA. The FPGA is powered from +3.3, linear-regulated from the +5 supply, but it connects to the CPU bus, and it's supposed to be 5-volt tolerant on its I/Os. So it looks like the +5 blew the CPU and it, in turn, pulled up a bunch of the FPGAs i/o pins and fried it too.

What's cool is that the CPU is running very hot but is still executing the firmware! And the FPGA is hot and really dead.

We're going to replace both chips (418 pins total!) and see how things look. There are 5 more FPGAs on the board, but we're optimistic they're OK.

Here's a pic of the board.

Re: kablooey

<http://www.highlandtechnology.com/DSS/V375DS.html>

John

\$30,000 repair

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| J. Larkin
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Hard Place piss off the customer
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My questions: what are you going to do on the repaired board to prevent a repeat? Can you offer to market that engineering change at x\$ per for the other boards that haven't yet been blown? What happens with the 5 other boards that are blown?

There must be a way to turn this into a public relations coup ...

Ed

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