

Re: kablooey

Source: <http://sci.tech-archive.net/Archive/sci.electronics.design/2006-03/msg01300.html>

- *From:* John Larkin <jjlarkin@xx>
 - *Date:* Fri, 10 Mar 2006 08:33:55 -0800
-

On Fri, 10 Mar 2006 18:46:16 +1000, The Real Andy
<will_get_back_to_you_on_This@> wrote:

On Thu, 09 Mar 2006 08:15:13 -0800, John Larkin
<jjlarkin@xx> wrote:

<snip>

Here's a pic of the board.

<http://www.highlandtechnology.com/DSS/V375DS.html>

John

Can i see to eproms toward the top?

Yes, I used two for faster (16-bit) program access. They hold the firmware and the config data for the six Xilinx FPGAs. And the 16-bit sine function, which takes another 128 kbytes!

John