



Re: Microcontroller; any suggestions?

So, speaking of TCP/IP on an fpga, if someone did that, what would be the recommended interface to, say, cat5?

You got to have a PHY (like Intel LXT972M) which essentially translates between 4 bits LVTTTL and baseband ethernet.

After the PHY comes 1:1 signaltransformer and various passive components.

However I suspect it's possible to interface 10 Mbps FD with an electrical hack useing LVTTTL I/O's. Not verified thoe. It would however be very touchy to any mishaps, but cheap! ;)

And the PHY needs a 25 MHz 50 ppm crystal.

.