

# Re: Replacing SRAM with a SIMM

---

*Source:* <http://sci.tech-archive.net/Archive/sci.electronics.design/2006-09/msg00540.html>

---

- *From:* "Kryten" <[kryten\\_droid\\_obfuscator@xxxxxxxxxxxxx](mailto:kryten_droid_obfuscator@xxxxxxxxxxxxx)>
  - *Date:* Sun, 03 Sep 2006 10:41:55 GMT
- 

"FyberOptic" <[fyberoptic@xxxxxxxxxx](mailto:fyberoptic@xxxxxxxxxx)> wrote in message  
[news:1157220878.647185.241200@xx](mailto:news:1157220878.647185.241200@xx)

I've got a 6502-based project

So my thought was to take one of these countless 30-pin SIMMs

Good news, there is an easy way to interface DRAM and 6502 chips. Though you wouldn't think it to look at some of the commercial designs I have seen, and listen to what most people say.

[http://www.howell1964.freemove.co.uk/projects/DRAM\\_6502.htm](http://www.howell1964.freemove.co.uk/projects/DRAM_6502.htm)

This circuit supports 16 to 8 address line multiplexing, and 8-bit refresh address. So a pair of 64Kx4 chips will give you a full memory map.

These days it is cheap and simple to fit a 32K or 128K SRAM chip, so it is only worth using DRAM for relatively large memories.

Fortunately it is easy to extend the circuit to have more refresh address bits and multiplex more address lines. The latter can be connected to a page-select latch.