

# Re: Replacing SRAM with a SIMM

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- *From:* "Kryten" <[kryten\\_droid\\_obfuscator@xxxxxxxxxxxxx](mailto:kryten_droid_obfuscator@xxxxxxxxxxxxx)>
  - *Date:* Sun, 03 Sep 2006 19:24:53 GMT
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"Michael Black" <[et472@xxxxxxxxxxxxxxxxxxxxx](mailto:et472@xxxxxxxxxxxxxxxxxxxxx)> wrote in message [news:edet28\\$61d\\$1@xxxxxxxxxxxxxxxxxxxxx](mailto:news:edet28$61d$1@xxxxxxxxxxxxxxxxxxxxx)

([cbarn24050@xxxxxxx](mailto:cbarn24050@xxxxxxx)) writes:

As people have pointed out in the past, cache ram off older "IBM Compatible" computer motherboards is static and 8bit wide and will supply lots of RAM for nothing when you find those boards in the garbage.

True, I have plenty of them if anyone wants one.

However to be fast they were also small (32K), and low power consumption was a secondary concern.

bank switching, which was always rather lame. Bank switching back then tended to be for data, not programs, since it was problematic to plan for more code being in the right place at the right time.

The BBC micro had an OS call for calling routines in other banks. The OP might be able to crib from that code.