

# Re: The HP Way

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*Source:* <http://sci.tech-archive.net/Archive/sci.electronics.design/2006-09/msg02562.html>

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- *From:* "Michael A. Terrell" <[mike.terrell@xxxxxxxxxxxxxx](mailto:mike.terrell@xxxxxxxxxxxxxx)>
  - *Date:* Thu, 14 Sep 2006 20:14:34 GMT
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John Larkin wrote:

VME has historically been ugly.

[http://www.getntds.com/product\\_images/product\\_465.jpg](http://www.getntds.com/product_images/product_465.jpg)

<http://www.cacdsp.com/products/images/v3c31.jpg>

<http://phys-ds.physics.lsa.umich.edu/docushare/dsweb/GetRendition/Document-1851/html/index5744123.jpg>

The tall skinny front panel, usually paved with connectors, doesn't help. Most people then silkscreen the panel with easily-chipped black ink lettering in random fonts and locations. The overlay thing is about as much hassle as sending out panels to be anodized and screened, and looks cool.

I've looked at your pictures and I agree. I have a handful of VME boards laying around, along with a dual connector extender card, and maybe a backplane.

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Service to my country? Been there, Done that, and I've got my DD214 to prove it.

Member of DAV #85.

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