

# Driving an OP AMP input with no VCC

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I have a similar question to one posted a few days ago. I am thinking of driving an OP AMP running at 5V configured as a follower with a 4.0V input via a 10K and there could be times where Vcc is removed while the input remains. Is it possible that this could damage the IC and should I use a 74LCX541 buffer as previously discussed?

Best regards,

AJ

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