

Re: Driving an OP AMP input with no VCC

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- *From:* Jim Thompson <To-Email-Use-The-Envelope-Icon@xxxxxxxxxxxxxxxx>
 - *Date:* Tue, 07 Nov 2006 17:51:33 -0700
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On Wed, 08 Nov 2006 00:37:57 GMT, "AJ" <itisme33@xxxxxxxxxxxxxxxx> wrote:

"Jim Thompson" <To-Email-Use-The-Envelope-Icon@xxxxxxxxxxxxxxxx> wrote in message news:7h8112hib1ioqps59k0lba5t7lrr8urk3@xxxxxxxxxxx

On Tue, 07 Nov 2006 11:38:23 GMT, "AJ" <itisme33@xxxxxxxxxxxxxxxx> wrote:

I have a similar question to one posted a few days ago. I am thinking of driving an OP AMP running at 5V configured as a follower with a 4.0V input via a 10K and there could be times where Vcc is removed while the input remains. Is it possible that this could damage the IC and should I use a 74LCX541 buffer as previously discussed?

Best regards,

AJ

Depends on the OpAmp. Read the maximum input voltage spec. If it defines maximum input as a relationship with VCC, then you may have problems.

Be more precise in you question... OpAmp type, etc.

...Jim Thompson

I am using and MCP6044 running at 5V and I have the input clamped via BAT54S

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diodes to a 3.3V rail and ground. The problem is that my board may lose power while an input remains. I have a TVS on the 3.3V rail to prevent it going above 3.3V but this would mean that an input would be clamped at 3.6V while the OP AMP's Vcc would be 0V.

Best regards

AJ

I am unable to view a data sheet from Microchip's site. Firefox just hangs and sucks 100% CPU. Sorry.

...Jim Thompson

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I love to cook with wine. Sometimes I even put it in the food.

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