

Re: AD7714 help

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- *From:* "Arlet" <usenet+5@xxxxxxxxxxxxxxxxxxxxx>
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Prakruthi wrote:

I can see constant clock signals on the oscilloscope for SCLK and DIN and DOUT shows data transfers, but the DRDY acknowledge pin on the AD7714 sometimes just shows noise and sometimes shows a low(which is an indication of dataword being available at the output register of the AD7714) after several several clock cycles.Do you understand what I mean?

Not sure what you mean. Can you link a picture ?

Also, the SCLK frequency is not constant. It keeps varying. Is that ok?

As long as you're not violating cycle, and setup/hold times of the AD7714, there's no problem for the communication, but the fact that it is varying at all sounds suspicious. It *should* be rock solid. Did you check basics stuff, such as whether your power supply is clean, and your main oscillator is stable ?

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