

Re: Resources regarding Phase Locked Loops?

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- *From:* "Michael" <nleahcim@xxxxxxxxxx>
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On Apr 11, 1:19 pm, Joerg <notthisjoerg...@xxxxxxxxxxxxxxxxxxxxxxxxxx> wrote:

Michael wrote:

Hi – I'm going to need to build a PLL circuit that can compare two signals in the MHz range and find the phase difference between the two. One will be very nice and clean while the other will be (I suspect) quite noisy. I have never, ever done anything with PLLs. My coursework at my uni (I am a senior EE, graduating in May) has not covered PLLs at all. I don't think the term has ever even been mentioned. I know about them from outside research.

Has never been mentioned? Ouch. What university was that?

Anyways – can anybody point me towards good resources regarding designing such a circuit?

If you've never done a PLL I suggest to get a copy of the ARRL Handbook. Mine are older but PLL is handled in there. If this is for a mass product and it's on a tight schedule get consulting help.

I think Analog Devices has some good app notes about the topic as well. So did Philips but then they messed up their web site.

It's not that PLLs aren't covered at all – they're covered in some classes, just none that I have taken. I took a pretty specialized courseload, and PLLs are far from my specialty. However, it looks as if I'm going to have to learn the buggers.

It's not for a mass product (at least not on a strict timeline – eventually I hope to bring this device to market)

I'll take a look at the ARRL handbook. I see it mentioned enough that I should check it out anyways.

Re: Resources regarding Phase Locked Loops?

-Michael

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