

# The Education of MassiveProng (Pt 3 of 275) (001/634) (587/634)

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- *From:* MassiveProngSucks. <[Massive@xxxxxxxxxxxxxxxxx](mailto:Massive@xxxxxxxxxxxxxxxxx)>
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by external PCI masters are controlled by external request/grant signals. Arbitration is based on a fair rotation scheme and allows for one master agent to be assigned as a super agent. The arbiter supports locked cycles by implementing a PCI bus lock.

The PPU contains an internal wholly on-chip fast-AT bus 904 that is similar to the ISA bus. However, the on-chip fast-AT bus is dynamically programmable and runs at higher than typical ISA bus speeds. Fast-AT bus speeds, for IDE accesses and non-IDE accesses, can be independently programmed to run at 1/2, 1/3, or 1/4 the PCI clock

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