

The Education of MassiveProng (Pt 3 of 275) (001/634) (483/634)

Source: <http://sci.tech-archive.net/Archive/sci.electronics.design/2007-04/msg05289.html>

- *From:* MassiveProngSucks. <Massive@xxxxxxxxxxxxxxxxx>
 - *Date:* Sat, 21 Apr 2007 01:04:55 -0700
-

The PCI bridge 716 provides a low-latency path through which the 486 core processor directly accesses other PCI agents mapped anywhere in memory and I/O spaces. Additionally, the PCI bridge 716 provides a high-bandwidth path that allows PCI masters outside MPU 102 direct access to main memory. MPU 102 is capable of behaving as a bus master (initiator) or PCI Slave (target) running at 0 MHz up to 25 or 33 MHz and much higher frequencies into hundreds of MegaHertz according to the concepts disclosed herein.

MPU 102 implements a 256-byte configuration space, which is a physical

.