

# Re: Painless micro program

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- *From:* krw <krw@xxxxxxxxxx>
  - *Date:* Sun, 20 May 2007 10:03:41 -0400
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In article <1179633027.463343.192350@xxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxx>, kensmith@xxxxxxxx says...

On May 19, 5:54 pm, krw <k...@xxxxxxxxxx> wrote:

In article <1179616199.332790.162...@xxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxx>, kensm...@xxxxxxxx says...

[... CDP1802 ...]

Well, the 8051, at least in its original incarnation, isn't much of a screamer either (12 clocks per op, as well).

There's a big difference between 12 cycles of a 12MHz clock and 12 cycles of a 2 MHz clock. Besides:

Like I said, there isn't much of a difference.

$A = B + C$

Compiles to something like 16 instructions if A, B and C are randomly located in the RAM area.

The 8051 is not so different if A, B, and C are randomly located in external RAM. I'd never use a HLL on such a beast.

[...]

Not very. Almost every computer keyboard on the planet has an "8048" in it.

I think today it is more of a 8048 like core in a special chip. I

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wouldn't be surprised to see only one package inside a keyboard. With the kind of quantity they are made in today a special purpose IC makes a lot of sense.

Which is why I put 8048 in quotes. My bet is that the 8048 core is still in there.

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Keith

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