

Re: Painless micro program

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- *From:* Rich Grise <rich@xxxxxxxxxxxx>
 - *Date:* Mon, 21 May 2007 18:43:26 GMT
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On Sun, 20 May 2007 09:55:18 -0700, John Larkin wrote:

There weren't many triple-operand machines around, ever.

In microprogrammable machines, three operands are almost routine, sometimes four: src A, src B, dest, and next instruction address.

They're fun! :-)

Cheers!
Rich

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