

Re: PCB Layout Designers

Source: <http://sci.tech-archive.net/Archive/sci.electronics.design/2007-05/msg03135.html>

- *From:* Robert Latest <boblatest@xxxxxxxxx>
 - *Date:* 24 May 2007 08:49:55 GMT
-

John Larkin wrote:

PADS will do a full connectivity check on this board in about 2 seconds

What's a connectivity check? I guess it's checking that all power pins are connected, that there are more than one pins on one net, that no two outputs share the same net, that a net not only has inputs on it, no open inputs... that sort of thing? It's called "electrical rule check" in my CAD.

You can also resequence the ref designators on the board

What's a "ref designator"? The "name" of the Part, such as U22, R4?

We never autoroute,

Ugh. Who does, anyway?

robert

.