

Re: DRAM data persistence

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On Jul 2, 4:34 pm, Richard Henry <pomer...@xxxxxxxxxxxx> wrote:

I remember years ago hearing about a security problem with DRAM, that data could partially persist in the DRAM cells through a power-off/power-on cycle and might be retrieved by careful application of power and reading of the DRAM contents. Does anyone remember details of this?

I have also seen it happen in static RAM. The data bits are not accurately remembered but there is a strong bias towards them waking up in the same state they were at power down.

I guess if someone can have access to the RAM every night after you go home, they may be able to reconstruct something of what you work on during the day.