

Re: IsThere a chip with multiple (4 or 8?) serial ports??

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- *From:* nospam <nospam@xxxxxxxxxxxxxxxx>
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"terry@xxxxxxxxxxxx" <terry@xxxxxxxxxxxx> wrote:

Any pointers to PIC code, suggestions, etc. would be appreciated.

One idea is not implementing 'serial' ports at all.

The raw bit stream for an 80 byte message packet sampled at say 8 times the baud rate could be recorded in about 1k of RAM.

If you play back that recording at 8 times the sample rate to an output 'serial' port you effectively forward the message you received at 4800 to the output at 38k4.

You need to detect the message packet boundaries which could probably be as crude as seeing a start bit at the beginning and x consecutive stop bits at the end.

If you need to identify which channel the packet was received on then it would be possible to 'play back' a bit of hard coded bit stream prior to the recorded bit stream.

A crude 8 to 1 and 4800 to 38k4 NMEA packet multiplexor. Hardware would be a fast uP with 8k of RAM and 9 I/O pins.

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