

# Re: Oscilloscope Shopping on a \$2000.00 Budget

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- *From:* Jan Panteltje <[pNaonStpealmtje@xxxxxxxxx](mailto:pNaonStpealmtje@xxxxxxxxx)>
  - *Date:* Sat, 06 Oct 2007 21:22:36 GMT
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On a sunny day (Sat, 06 Oct 2007 20:12:33 GMT) it happened D from BC <[myrealaddress@xxxxxxxxx](mailto:myrealaddress@xxxxxxxxx)> wrote in <[3gqfg359lb9va92fe236m147nfqgvuk9ge@xxxxxxxxx](mailto:3gqfg359lb9va92fe236m147nfqgvuk9ge@xxxxxxxxx)>:

I guess stay away from the Nagafogitosushi scopes :)

D from BC

With a 200\$ budget, and time on your hands, how about designing and building your own. As others mentioned you perhaps want a logic analyser for the uc too. Le's see:

Grab one LCD (color?) display.

Grab a fast large FPGA

FPGA has enough I/O pins for analyser and LCD, memory interface.

Grab the free Altera or Xilinx soft.

Grab a very fast AD, with some very fast SRAM.

The fast FPGA may have already a processor.

Now input attenuator, some buttons, simple power supply, and perhaps the most expensive the case.

Some connectors.

Some input protection.

Anybody have any design ideas?