

Re: Debouncing....at About 1Mhz

Source: <http://sci.tech-archive.net/Archive/sci.electronics.design/2007-11/msg00249.html>

- *From:* John Fields <jfields@xxxxxxxxxxxxxxxxxxxxxx>
 - *Date:* Fri, 02 Nov 2007 17:34:18 -0500
-

On Fri, 02 Nov 2007 13:22:44 -0700, John Larkin
<jjlarkin@xxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxx> wrote:

On Fri, 02 Nov 2007 14:44:11 -0500, John Fields
<jfields@xxxxxxxxxxxxxxxxxxxxxxxx> wrote:

On Fri, 02 Nov 2007 11:20:51 -0700, John Larkin
<jjlarkin@xxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxx> wrote:

On Fri, 02 Nov 2007 11:23:12 -0500, John Fields
<jfields@xxxxxxxxxxxxxxxxxxxxxxxx> wrote:

The point isn't that they have Q and Qbar,
it's that you made a
mistake by specifying, verbally and
graphically, that the output be
taken from Q.

What the hell is your problem? I suggested a circuit to solve
a
problem, for free, and someone else, not you, pointed out
that the
output is inverted. It is: So what? Use Qbar or swipe an xor
section
and fix it, with my blessings. All you pointed out was that
you didn't
understand the clock chain.

My ego isn't invested in this, but apparently yours is.

Re: Debouncing....at About 1Mhz

Really? I readily admitted that I made a mistake by glossing over the clock chain, but you seem to be getting hot under the collar for having been called to account for committing the same sort (one would hope) of error regarding the direction of the output.

I'm not hot,

Then: "What the hell is your problem?" is your normal tone of voice?

and you can't "call me to account" because I don't answer to you.

Well, of course you don't have to answer to me, but you certainly have to account for your errors.

If you want to keep your private score, go for it.

Geez, John, thanks for your permission!

And yes, they're fast, but not fast enough to guarantee an input-to-output delay of $\leq 10\text{ns}$ without testing the devices for speed. That is, unless you know of a faster CMOS logic family than FACT. Do you?

Sure, several. FACT is decades old.

Geez, then, instead of all those cutesy pussyfooting tease tactics, why don't you just give the OP a break and specify what you had in mind? It is, after all, your design, isn't it? Or does support not come with it?

Re: Debouncing....at About 1Mhz

It's not a design, it's a suggestion. It doesn't cost anything and it doesn't come with a warranty.

I've heard that one shouldn't look a gift horse in the mouth, so if it doesn't work it's not your fault?

If you want logic, look at ONsemi, Fairchild, or TI. They have some.

Anything that'll support that originally asked for $<\sim 10\text{ns}$ input-to-output delay without culling?

--
JF
.