

# Re: SMPS inverter voltage feedback methods

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*Source:* <http://sci.tech-archive.net/Archive/sci.electronics.design/2008-01/msg04586.html>

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- *From:* Jamie Morken <[jmorken@xxxxxxx](mailto:jmorken@xxxxxxx)>
  - *Date:* Tue, 29 Jan 2008 07:09:25 GMT
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MooseFET wrote:

On Jan 27, 4:09 pm, Terry Given <[my\\_n...@xxxxxxx](mailto:my_n...@xxxxxxx)> wrote:

Joerg wrote:

MooseFET wrote:

[...]

To monitor  
the  
waveform?  
Why not  
just sample  
it? Send  
sampling  
pulse  
through  
toroid xfmr,

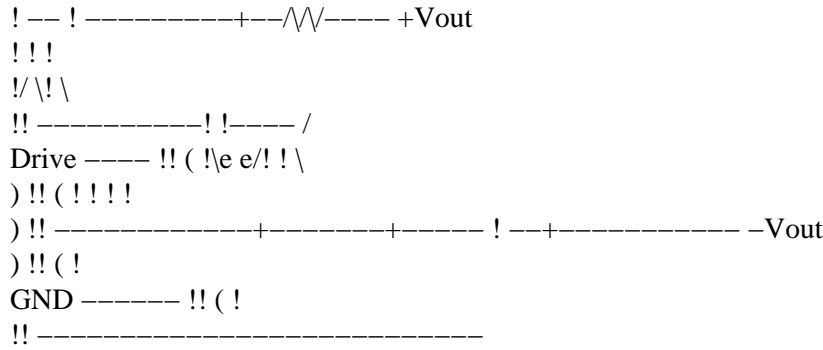
[...]

I cant quite picture it, would you care to cough up an ascii schematic?  
didnt unitrode make a chipset that did pretty much this?

Here's what was rattling through my mind while I was thinking about  
how to do this.

```
ACFB+ !! ACFB-  
+-^--+  
!!  
((((((  
=====  
((((((  
!!!
```

Re: SMPS inverter voltage feedback methods



This works even in the AC case if the resistors bring the voltage low enough that the EB junctions aren't breaking down while the transistors trade off conducting.

Hi,

I put this schematic into Itspice and it seems to kind of work, the AC voltage is being chopped by the Drive signal to Vout, but it is 90 degrees out of phase with the input AC signal, and also very low amplitude. Any ideas how to get the output phase to match the AC input?

Heres the circuit and waveform:

- <http://rocketresearch.nekrom.com/new/transformer%20chopper%20voltage%20measurement/circuit1.jpg>
- <http://rocketresearch.nekrom.com/new/transformer%20chopper%20voltage%20measurement/waveform1.jpg>

Itspice file:

- <http://rocketresearch.nekrom.com/new/transformer%20chopper%20voltage%20measurement/circuit1.asc>

cheers,  
Jamie

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