

## Re: current-mode opamps

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*Source:* <http://sci.tech-archive.net/Archive/sci.electronics.design/2008-02/msg03600.html>

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- *From:* Jim Thompson <[To-Email-Use-The-Envelope-Icon@xxxxxxxxxxxxxxxxx](mailto:To-Email-Use-The-Envelope-Icon@xxxxxxxxxxxxxxxxx)>
  - *Date:* Wed, 27 Feb 2008 07:58:46 -0700
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On Tue, 26 Feb 2008 21:30:20 -0800, John Larkin  
<[jjlarkin@xx](mailto:jjlarkin@xx)> wrote:

On Tue, 26 Feb 2008 20:48:30 -0800, "Jim Thompson"  
<[To-Email-Use-The-Envelope-Icon@xxxxxxxxxxxxxxxxx](mailto:To-Email-Use-The-Envelope-Icon@xxxxxxxxxxxxxxxxx)> wrote:

"John Larkin" <[jjlarkin@xx](mailto:jjlarkin@xx)> wrote in  
message  
[news:4te8s3h7h4037d3mgmqcroh4a9au5npndk@xxxxxxxxxxx](mailto:news:4te8s3h7h4037d3mgmqcroh4a9au5npndk@xxxxxxxxxxx)

On Tue, 26 Feb 2008 11:29:06 +0100, Fred Bartoli <" ">  
wrote:

[snip]

Are your supplies symmetrical or do you use  
it single supplied?

+7.5 and -5.5, so we can make anything from TTL to ECL  
output levels.  
But that shou;dn't affect input impedance symmetry.

John

Not so dummy, when you screw the TTL logic with wrong level you screw  
your  
output as well. What planet are you from?

Re: current-mode opamps

We speak English here. What planet are you from?

John

John, You \_are\_ aware that this is an "@aioe.org" forgery.

...Jim Thompson

—

James E. Thompson, P.E.	mens
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