

Re: breadboarding fast, tiny stuff

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"John Larkin" <jjlarkin@xx> wrote in message news:eu3rs3hc7l0bfjv8ujcfuju3vlsfp2oqvg@xxxxxxxx

Sometimes the fpga
blocks get big, too big for, say, a 4 mbit eprom, so our rom builder
program compresses them and the uP decompresses at runtime...

I expect that sooner or later Xilinx will add decompression hardware to the
FPGAs themselves to take advantage of that (significant!) gain you're
seeing... they already have some versions that decrypt the bitstream, as I
recall, adding decompression to that seems pretty straightforward.

---Joel

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