

Re: breadboarding fast, tiny stuff

Source: <http://sci.tech-archive.net/Archive/sci.electronics.design/2008-03/msg00741.html>

- *From:* Joerg <notthisjoergsch@xxxxxxxxxxxxxxxxxxxxxxxxxxxx>
 - *Date:* Tue, 04 Mar 2008 18:27:09 GMT
-

John Larkin wrote:

On Tue, 4 Mar 2008 09:44:17 -0800, "Joel Koltner"
<zapwireDASHgroups@xxxxxxxxxx> wrote:

"Joerg" <notthisjoergsch@xxxxxxxxxxxxxxxxxxxxxxxxxxxx> wrote in message
[news:kKfzj.14592\\$0o7.6510@xxxxxxxxxxxxxxxxxxxxxxxxxxxx](mailto:news:kKfzj.14592$0o7.6510@xxxxxxxxxxxxxxxxxxxxxxxxxxxx)

Same reason as with video storage formats and such.
Whatever wins in the marketplace is going to be used in
industry. And us consultants must use what industry uses.

"tar" also doesn't do any compression whatsoever -- it just packs everything
together into one file ("Tape ARchive"). The more contemporary *NIX file
format is .tgz ("tarred and then gZiped").

If you really want to go non-mainstream, formats such as .ace (WinAce) can
often gain an additional, say, 10% over Zip.

---Joel

Not to change the subject (horrors!) but I did a nice little
compression thing for Xilinx configuration streams. We build roms that
have uP application code and one or more fpga config data blocks, so
that the uP configures the fpga's at powerup. Sometimes the fpga
blocks get big, too big for, say, a 4 mbit eprom, so our rom builder
program compresses them and the uP decompresses at runtime. The
resulting rom space is 0.2 to 0.5 of the original size and bit-bangs
faster than the uncompressed version. The key here is that the config
data has long runs of zeroes.

One of the new Virtex parts has something like 80 megabits of config
space!

Re: breadboarding fast, tiny stuff

Pretty soon kids will need 80M to do an LED blinker :-)

—

Regards, Joerg

<http://www.analogconsultants.com/>

.