

# Re: uC selection

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*Source:* <http://sci.tech-archive.net/Archive/sci.electronics.design/2008-03/msg03008.html>

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- *From:* Spehro Pefhany <[speffSNIP@xxxxxxxxxxxxxxxxxxxxxxxxxxxx](mailto:speffSNIP@xxxxxxxxxxxxxxxxxxxxxxxxxxxx)>
  - *Date:* Mon, 17 Mar 2008 21:35:57 -0500
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On 17 Mar 2008 19:27:25 -0400, the renowned DJ Delorie  
<[dj@xxxxxxxxxxxx](mailto:dj@xxxxxxxxxxxx)> wrote:

nico@xxxxxxxxxxxx (Nico Coesel) writes:

Free and student versions are crippled on  
purpose (not all  
optimisation options available).

So? Download the sources and build yourself an uncrippled  
version.

That is not so easy because MIPS is not the most common architecture.  
I'm working on getting GCC 4.1.2 + Glibc + binutils compiled for MIPS  
using crosstool but there are some problems with getting the right  
versions together and using the proper settings.

Microchip is required to provide you with the sources to THEIR tools,  
so you shouldn't have to worry about getting the right versions.  
Download Microchip's gcc sources and build it, then use it in place of  
the student version. This is what the GPL is all about.

If you can't get the exact sources to the distributed Microchip gcc,  
then Microchip is not obeying gcc's license.

AFAIUI, the optimizer has nothing to do with gcc, hence they are not  
obliged to provide the source.

Best regards,  
Spehro Pefhany

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"it's the network..." "The Journey is the reward"

Re: uC selection

speff@xxxxxxxxxxxx Info for manufacturers: <http://www.trexon.com>

Embedded software/hardware/analog Info for designers: <http://www.speff.com>

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