

# Re: Digital wireless systems

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  - *Date:* Fri, 16 May 2008 08:21:52 -0700 (PDT)
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Hi,

Each bit of the 48 bit data gets out of the FPGA at the rising edge of the 1.5MHz of the Clock. I found following two transceiver chips

[http://www.nordicsemi.com/files/Product/applications/nAN24-12Software\\_Examples\\_Using\\_ShockBurst\\_Modesv1.0](http://www.nordicsemi.com/files/Product/applications/nAN24-12Software_Examples_Using_ShockBurst_Modesv1.0)

<http://www.chipcatalog.com/Datasheet/A1621136395209573B4A7AEFE50273A1.htm>

What do guys think? Has anybody worked with these chips before?

John

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