



Re: Ever heard of Potato Semiconductor?

and a quick patent search turns up nothing.

How can a simple inverter have a 0.8 ns rise/fall time and be good for 1.1 GHz? Their datasheets are very poorly specified, but their definition of "risetime" is nonstandard, something like 25–75%.

They spec their max frequencies with 2 pF loads, so one of their parts won't drive even one of their own parts at the promoted speeds; what good is that?

One might almost suspect them of rebranding other parts. There are lots of other peoples' parts with typical performance like this, some better.

One problem with running CMOS at high speeds is power consumption. They don't mention this in any usable form.

Their English is atrocious.

John

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