

Re: Ever heard of Potato Semiconductor?

Source: <http://sci.tech-archive.net/Archive/sci.electronics.design/2008-07/msg00795.html>

- *From:* Tim Wescott <tim@xxxxxxxxxxxxxxxxxxxx>
 - *Date:* Sun, 06 Jul 2008 12:27:26 -0700
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Jim Thompson wrote:

On Sun, 06 Jul 2008 11:42:09 -0700, John Larkin
<jjlarkin@xx> wrote:

On Sun, 06 Jul 2008 11:28:23 -0700, Jim Thompson
<To-Email-Use-The-Envelope-Icon@xxxxxxxxxxxxxxxx> wrote:

-- snip --

Pico-second speeds in CMOS are power hogs... my usual design cross-over point is around 250-300MHz.

In a WiFi repeater chip I come from 5GHz down to 250MHz in CML, then CMOS.

...Jim Thompson

Yes, but you went to school back when "circuit design" meant "circuit design", not "simulating things you don't understand on software you don't understand using a process you don't understand."

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